

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): So, Hock C.; Wang, Sau C.
Assignee: SanDisk Corporation
Title: Multi-Bit-Per-Cell Flash EEPROM Memory With Refresh
Serial No.: 09/680,797 Filing Date: October 6, 2000
Examiner: Not Assigned Group Art Unit: Not Assigned
Docket No.: M-10277-3C US

11002 U.S. PRO
10/045505
10/18/01

San Francisco, California
October 18, 2001

COMMISSIONER FOR PATENTS
Washington, D.C. 20231

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR § 1.97(b)**

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying form PTO-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents have been submitted in prior U.S. Application Serial No. 09/680,797, filed on October 6, 2000, which is a Continuation of U.S. Application Serial No. 09/200,220, filed on November 25, 1998, which is a Continuation-in-Part of U.S. Application Serial No. 08/924,909, filed on September 8, 1997, from which this Application claims priority therefrom, and therefore, are not enclosed.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;

2. a representation that a search has been made; or

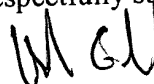
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3. an admission that the information cited herein is, or is considered to be,
material to patentability as defined in § 1.56(b).

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Respectfully submitted,



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FORM PT01449
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ATTY. DOCKET NO.

-M-10277-3C

SERIAL NO.

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INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANT

Hock C. So and Sau C. Wong

FILED

October 18, 2001

GROUP

Not Assigned

U. S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER								DATE	NAME	CLASS	SUB CLASS	FILING DATE
	A1	4	4	6	0	9	8	2		7/17/1984	Gee et al.			
	A2	4	6	1	2	6	3	0		9/16/1986	Rosier			
	A3	4	9	6	4	0	7	9		10/16/1990	Devin			
	A4	5	1	5	1	9	0	6		9/29/1992	Sawada			
	A5	5	1	5	7	6	2	9		10/10/1992	Sato et al.			
	A6	5	2	3	9	5	0	5		8/24/1993	Fazio et al.			
	A7	5	3	9	4	3	5	9		2/28/1995	Kowalski			
	A8	5	5	2	3	9	7	2		6/04/1996	Rashid et al.			
	A9	5	5	3	2	9	6	2		7/02/1996	Auclair et al.			
	A10	5	5	8	3	8	1	2		12/10/1996	Harari			
	A11	5	6	5	2	7	2	0		7/29/1997	Aulas et al.			
	A12	5	6	8	9	4	6	5		11/1997	Sukegawa et al.			
	A13	5	7	5	1	6	3	9		5/1998	Ohsawa			
	A14	5	7	6	1	1	2	5		6/02/1998	Himeno			
	A15	5	8	3	5	4	1	3		11/10/1998	Hürter et al.			
	A16	5	9	0	9	4	4	9		6/01/1999	Soret al.			

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER								DATE	COUNTRY	CLASS	SUB CLASS	TRANS.? (YES/NO)
	B1	WO	9	0	1	2	4	0	0	10/18/199	W.I.P.O.			

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.)

	C1	Lee, E.K.F., et al., "Error Correction Technique For Multivalued MOS Memory," <u>Electronic Letters</u> , Vol. 27, No. 15 (July 18, 1991) pps. 1321-1323												

EXAMINER

DATE CONSIDERED

* EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

U.S. Department of Commerce, Patent and Trademark Office	Atty Docket No.	Serial No.
	M-10277-3C US	Not Assigned
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Applicant(s)	
(Use several sheets if necessary)	Hock C. So, Sau C. Wong,	
	Filing Date	Group
	October 18, 2001	Not Assigned

U.S. Patent Documents

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	6,151,246	11/2/2000				
	5,712,815	01/1998	Bill et al.			

Foreign Patent Documents

							Translation	
	Document	Date	Country	Class	Subclass		Yes	No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner

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